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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/761,910	01/17/2001	Fred Chu	Adtran.2001.01	6132
24504	7590	06/29/2004	EXAMINER	
THOMAS, KAYDEN, HORSTEMEYER & RISLEY, LLP 100 GALLERIA PARKWAY, NW STE 1750 ATLANTA, GA 30339-5948			BURD, KEVIN MICHAEL	
		ART UNIT	PAPER NUMBER	11
		2631		

DATE MAILED: 06/29/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	09/761,910	CHU ET AL.	
	Examiner	Art Unit	
	Kevin M Burd	2631	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 11 December 2003.
 2a) This action is **FINAL**. 2b) This action is non-final.
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-57 is/are pending in the application.
 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
 5) Claim(s) 12-20 and 57 is/are allowed.
 6) Claim(s) 1-11 and 21-56 is/are rejected.
 7) Claim(s) _____ is/are objected to.
 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
 10) The drawing(s) filed on 01 March 2001 is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| | 6) <input type="checkbox"/> Other: _____ |

Information Disclosure Statement

1. The information disclosure statement filed 1/17/2001 fails to comply with the provisions of 37 CFR 1.97, 1.98 and MPEP § 609 because no 1449 form has been submitted and no copies of the references are found. It has been placed in the application file, but the information referred to therein has not been considered as to the merits. Applicant is advised that the date of any re-submission of any item of information contained in this information disclosure statement or the submission of any missing element(s) will be the date of submission for purposes of determining compliance with the requirements based on the time of filing the statement, including all certification requirements for statements under 37 CFR 1.97(e). See MPEP § 609 ¶ C(1).

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-11 and 21-56 are rejected under 35 U.S.C. 102(b) as being anticipated by Nicolas et al (US 5,453,797).

Regarding claims 1, 3, 5, 7-10, 21, 23, 25-30, 32, 34-39, 41, 43, 45-49, 51 and 53-56 Nicolas discloses the apparatus for noise reduction as shown in figure 13. A

trellis decoder 1322 is disclosed and an error signal is output from adder 1320. A linear feed forward equalizer 1306 receives an input signal and is operatively coupled to the trellis decoder 1322. The error signal updates the coefficients of the equalizer as shown in figure 13. A feedback equalizer 1324 (noise predictor) is operatively coupled to the trellis decoder 1322 and equalizer 1306. An error signal is input to the noise predictor from adder 1338 and the coefficients of the noise predictor are updated by the error signal output from adder 1320. The output of the noise predictor is added to the output of the equalizer 1306 in adder 1332.

Regarding claims 2, 22, 31, 40 and 50, the trellis decoder selects the best path and thereby the path with the least error.

Regarding claims 4, 24, 33 and 42, the delays are shown in elements 1318 and 1336 in figure 13.

Regarding claims 6, 44 and 52, the noise predictor is a linear feedback equalizer.

Regarding claim 11, the LMS algorithm is used (column 12, lines 48-50).

Allowable Subject Matter

3. Claims 12-20 and 57 are allowed.

Conclusion

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Chu (US 6,687,235) discloses a system for eliminating noise using a trellis decoder as shown in figure 12. This circuit does not contain the noise

predictor of the independent claims. Tan et al (US 6,438,164) discloses the noise reducing system shown in figure 22. The decision circuit 310 outputs an error signal, which is input to the FFE. The noise predictor of the independent claims is not disclosed. Ghosh (US 5,777,692) in figure 7 and Gadot et al (US 5,513,216) disclose similar systems as disclosed above. A decision circuit creates and error signal for updating a FFE in addition to updating a feedback filter.

Contact Information

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks
Washington, D.C. 20231

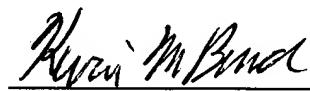
or faxed to:

(703) 872-9314, (for formal communications intended for entry or for informal or draft communications, please label "PROPOSED" or "DRAFT")

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA, Sixth Floor (Receptionist).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kevin Burd, whose telephone number is (703) 308-7034. The Examiner can normally be reached on Monday-Thursday from 9:00 AM - 6:00 PM.

Any inquiry of a general nature or relating to the status of this application should be directed to the Group receptionist whose telephone number is (703) 305-3800.



Kevin M. Burd
PATENT EXAMINER
6/27/2004